



# Seminarankündigung

**Dienstag, 20. Dezember 2011  
17:15 Uhr**

**WSI, Seminarraum S 101**

## **“Si and Ge nanowires as building blocks for novel devices”**

In order to continue Moore's law new materials for electronic components are being developed. Nanowires have been suggested to pose novel properties for applications in electronic devices, in particular in future computer electronics. The purpose of the presentation is to illuminate several aspects regarding the synthesis of silicon and germanium nanowires, their electrical properties, and the fabrication of a first device made thereof. Following an introductory survey of nanowire growth methods, experimental results concerning the epitaxial growth of nanowires are presented. The diameter dependence of the nanowire growth velocity and crystallographic growth direction of nanowires, a parameter that is of great importance especially in view of the technical applicability, will be discussed. After these partially theoretical considerations with regard to the nanowire morphology, the electrical and optical properties of nanowires will be shown. Having electronic applications of these nanowires in mind, the fabrication of a nanowire field-effect transistor is naturally the first step. The feasibility of the fabrication process and the basic functionality of nanowire based devices such as gate all around FETs or “straintronic” devices will be demonstrated.

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